



**TIMED - GUVI**

**Thermosphere-Ionosphere-Mesosphere-Energetics and Dynamics**  
**Global UltraViolet Imager**

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## Focal Plane Electronics

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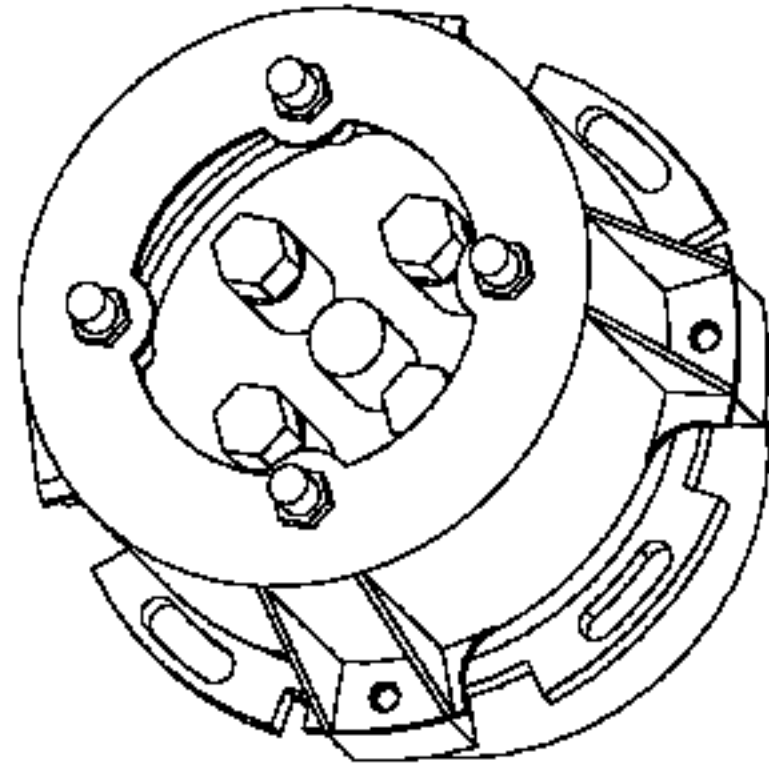
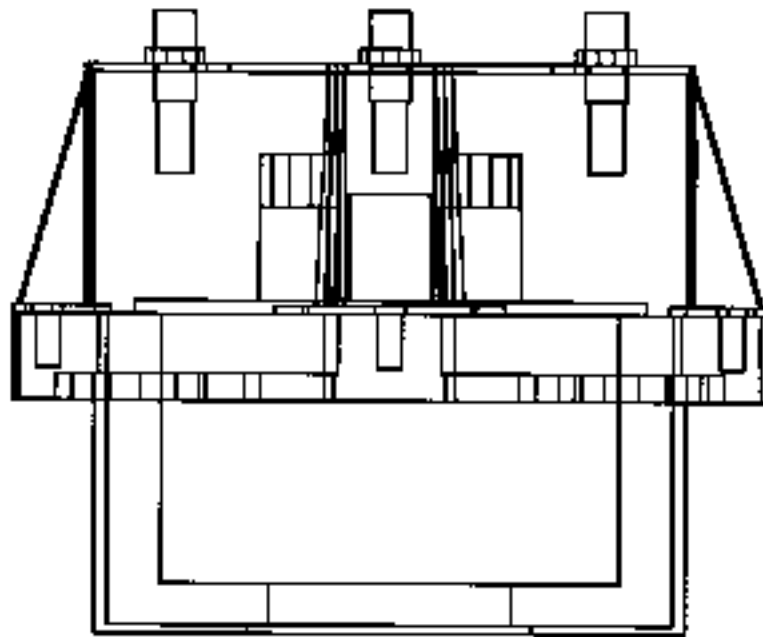


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### UV Detector Connections

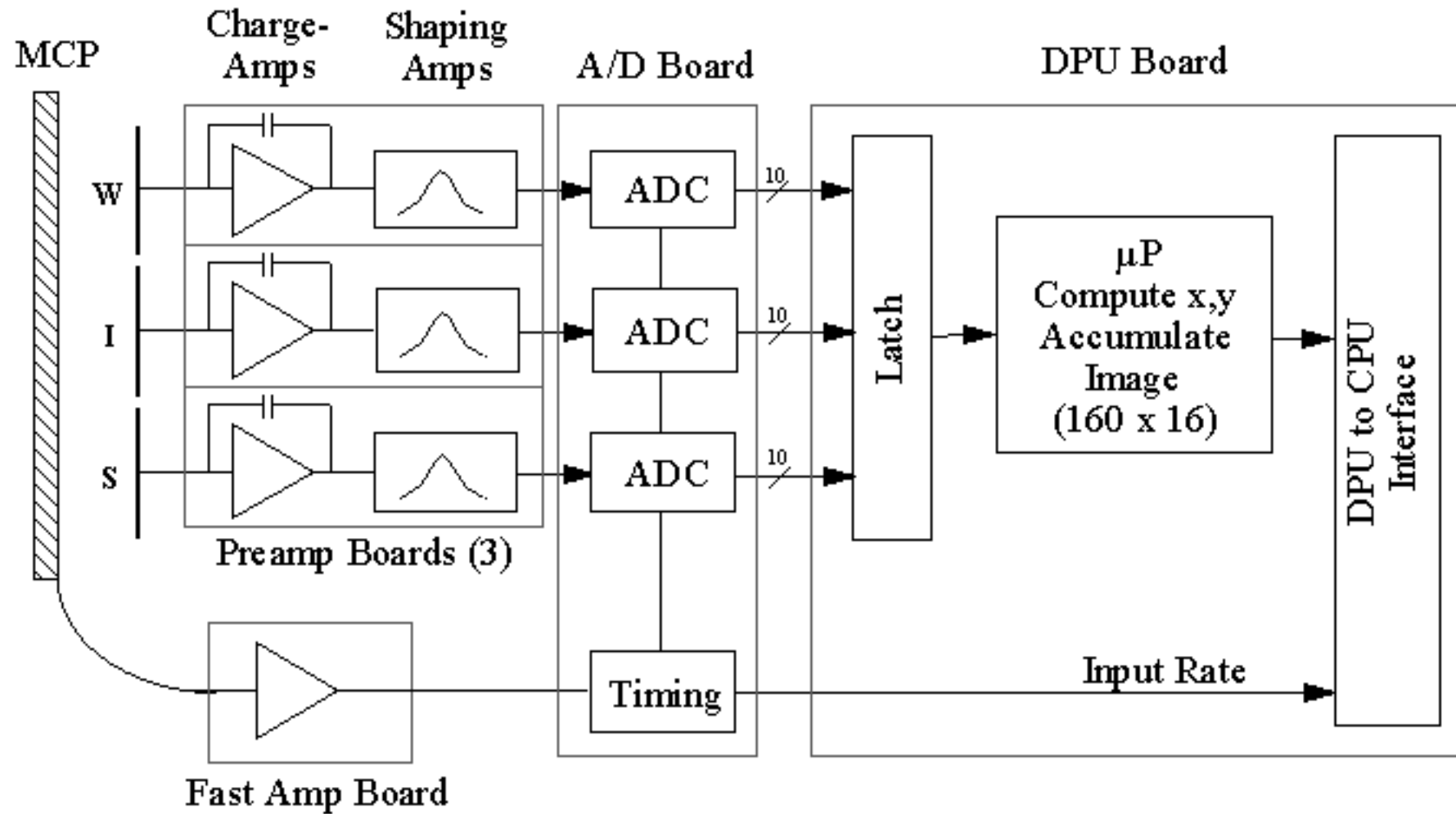




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## Detector Electronics



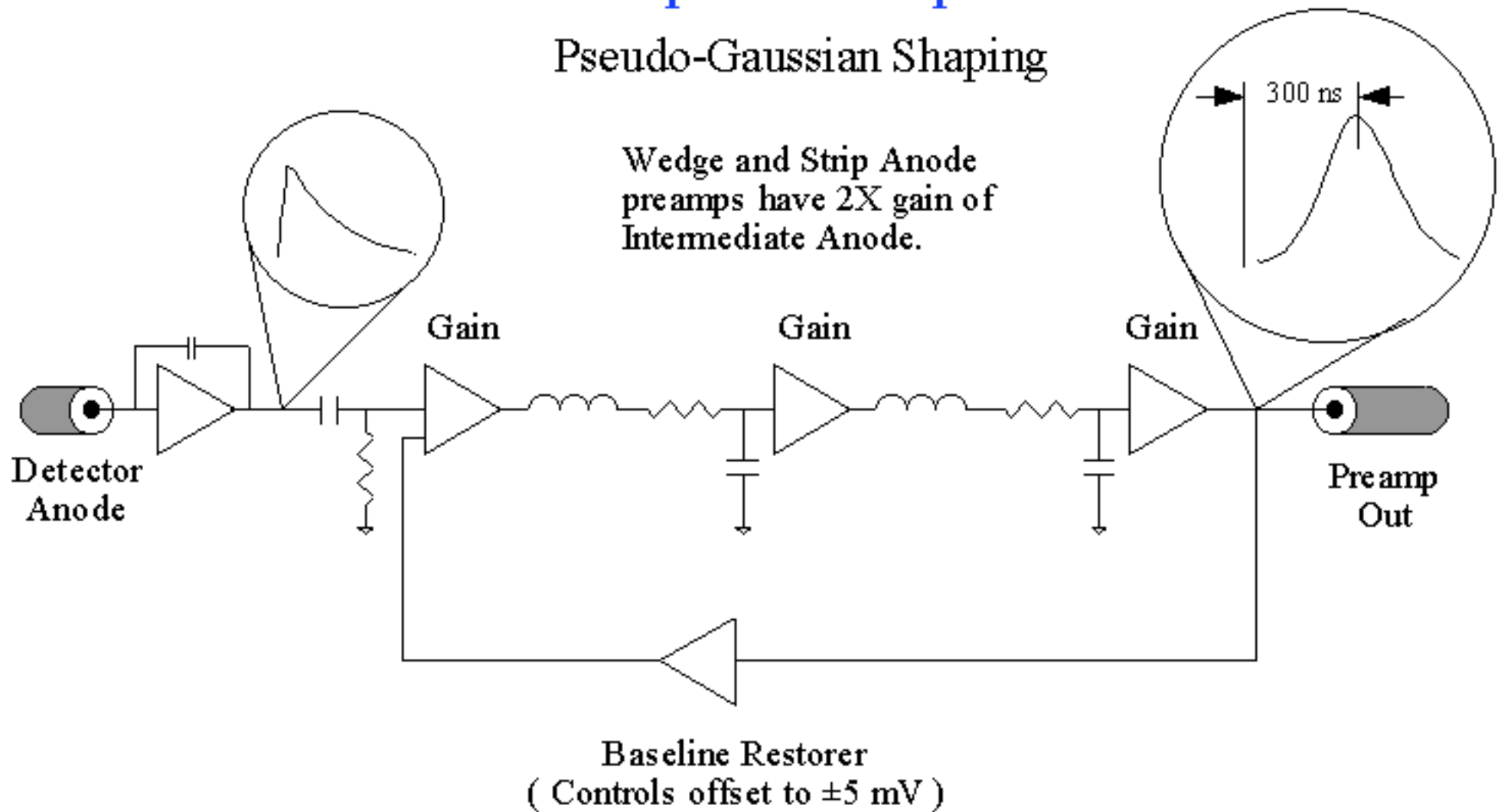


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## Preamplifier/Shaper

### Pseudo-Gaussian Shaping



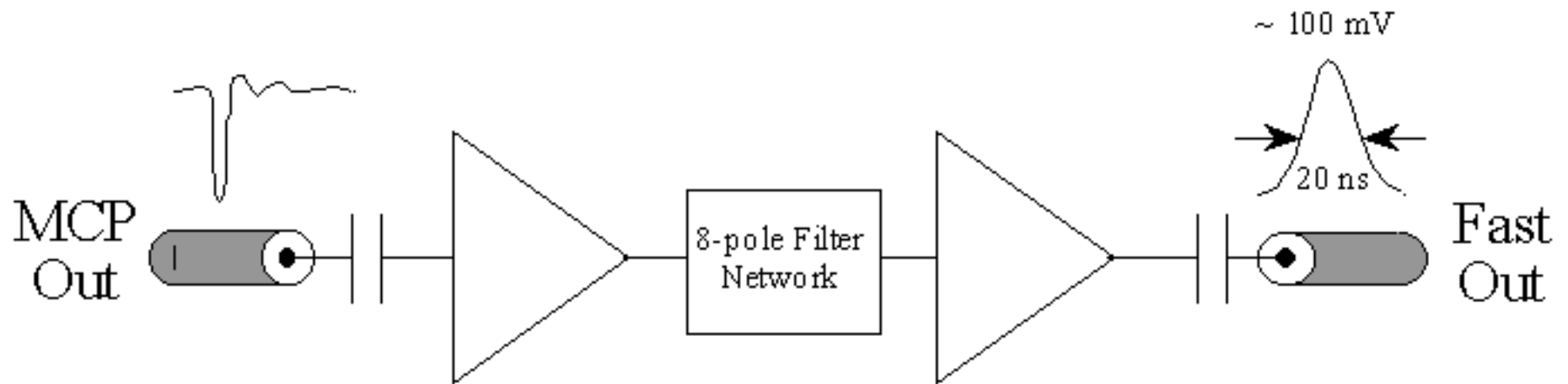


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### Fast Amp



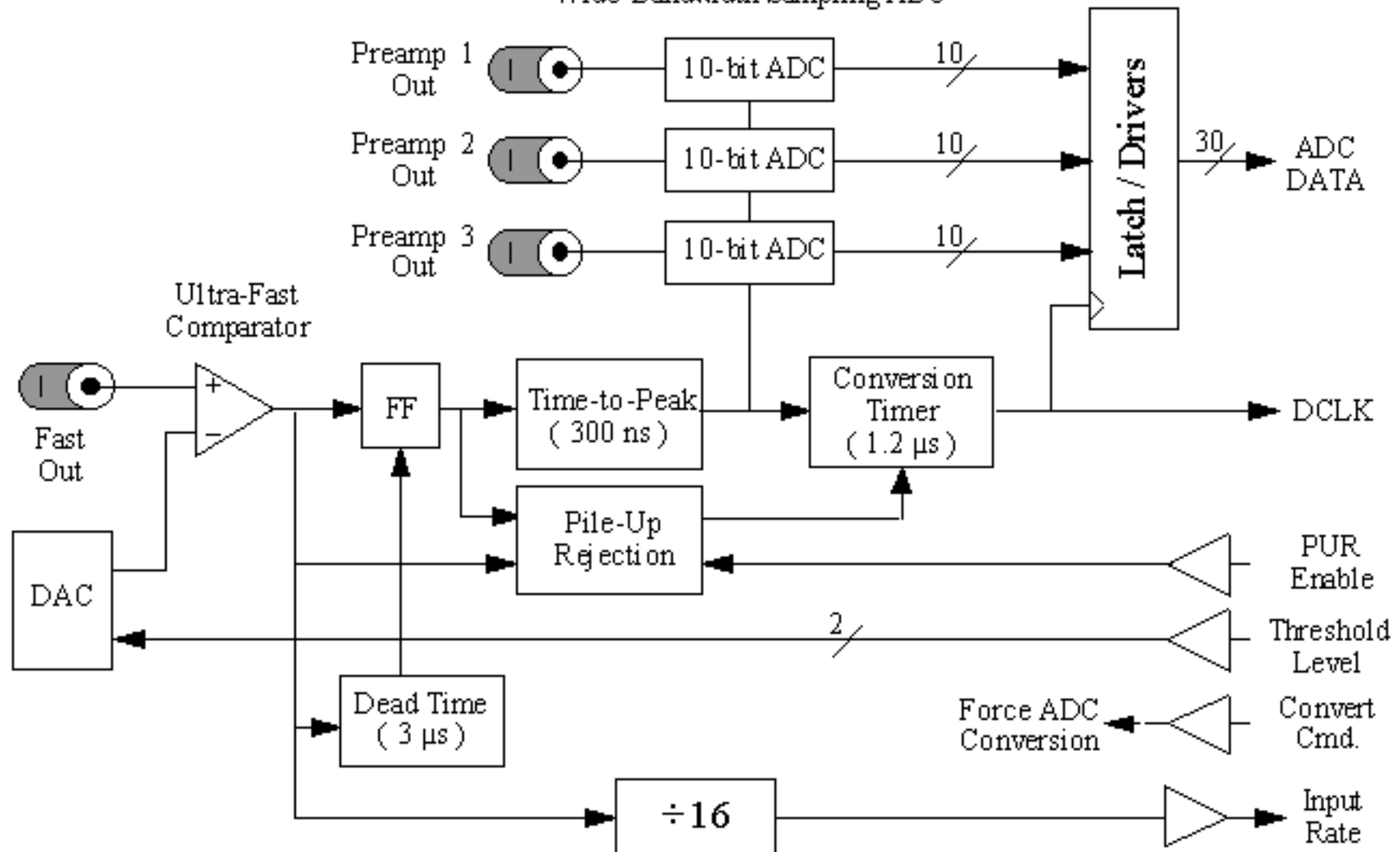


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## Pulse Height Analyzer

Wide-Bandwidth Sampling ADC

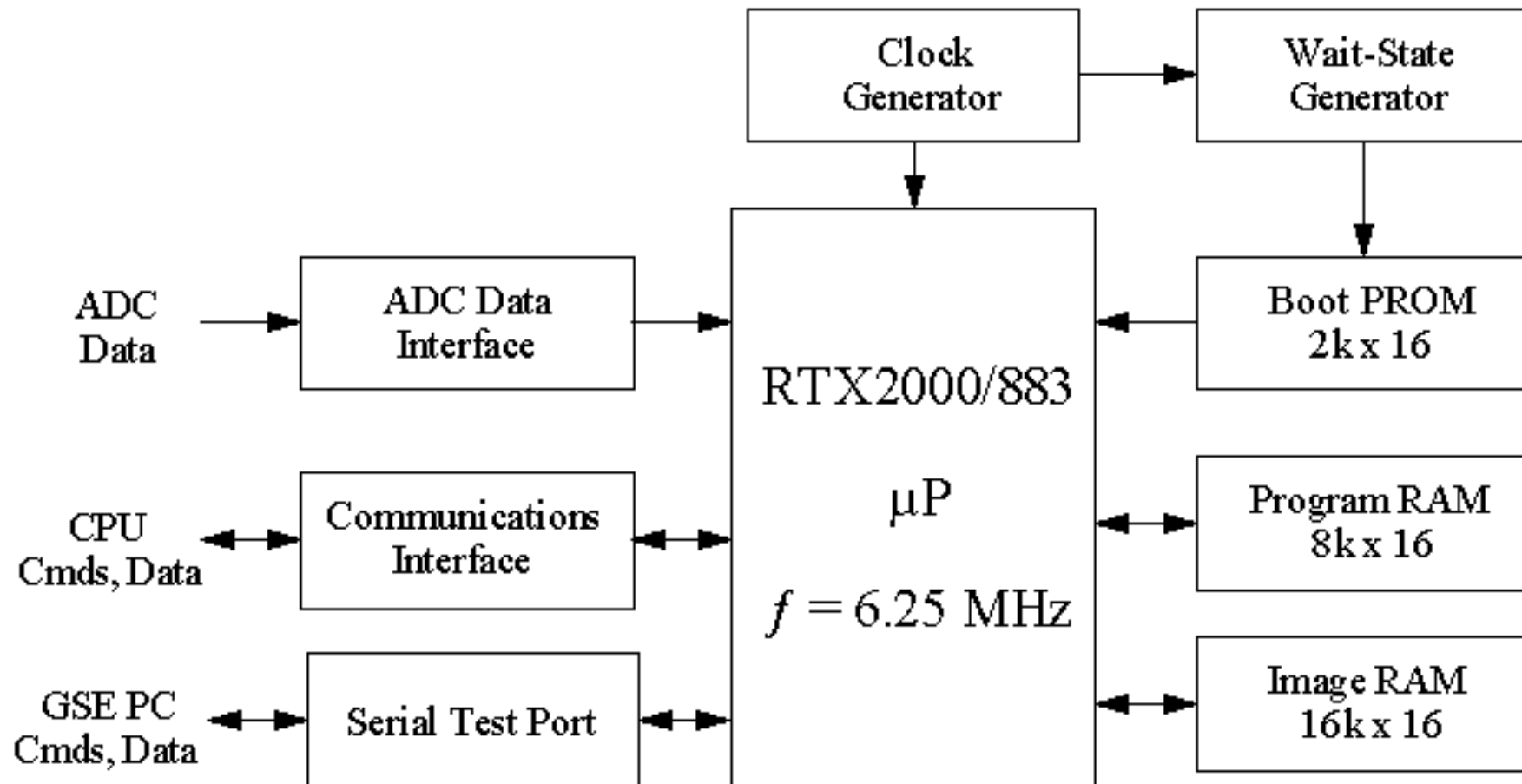




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### Detector Processor

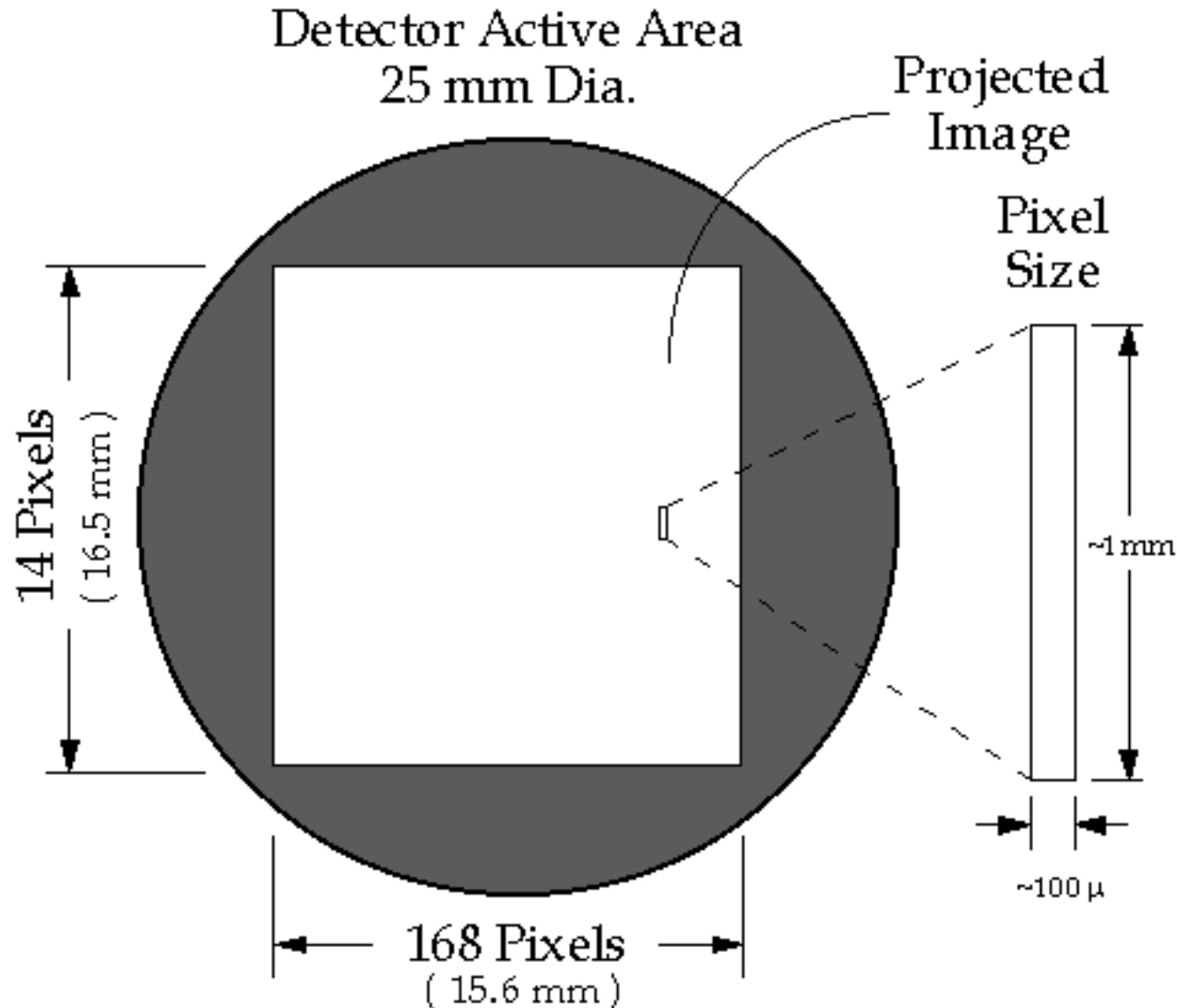




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### UV Detector - Focal Plane







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### Detector Data Processing

Poll for new event

Read W, S, I A/D values, check bounds

Compute x-y position

$$\text{Spectral coordinate} = \frac{aW}{W+S+2I} - \text{offset}$$

$$\text{Spatial coordinate} = \frac{bS}{W+S+2I} - \text{offset}$$

Quantize Results

8-bit Spectral

4-bit Spatial

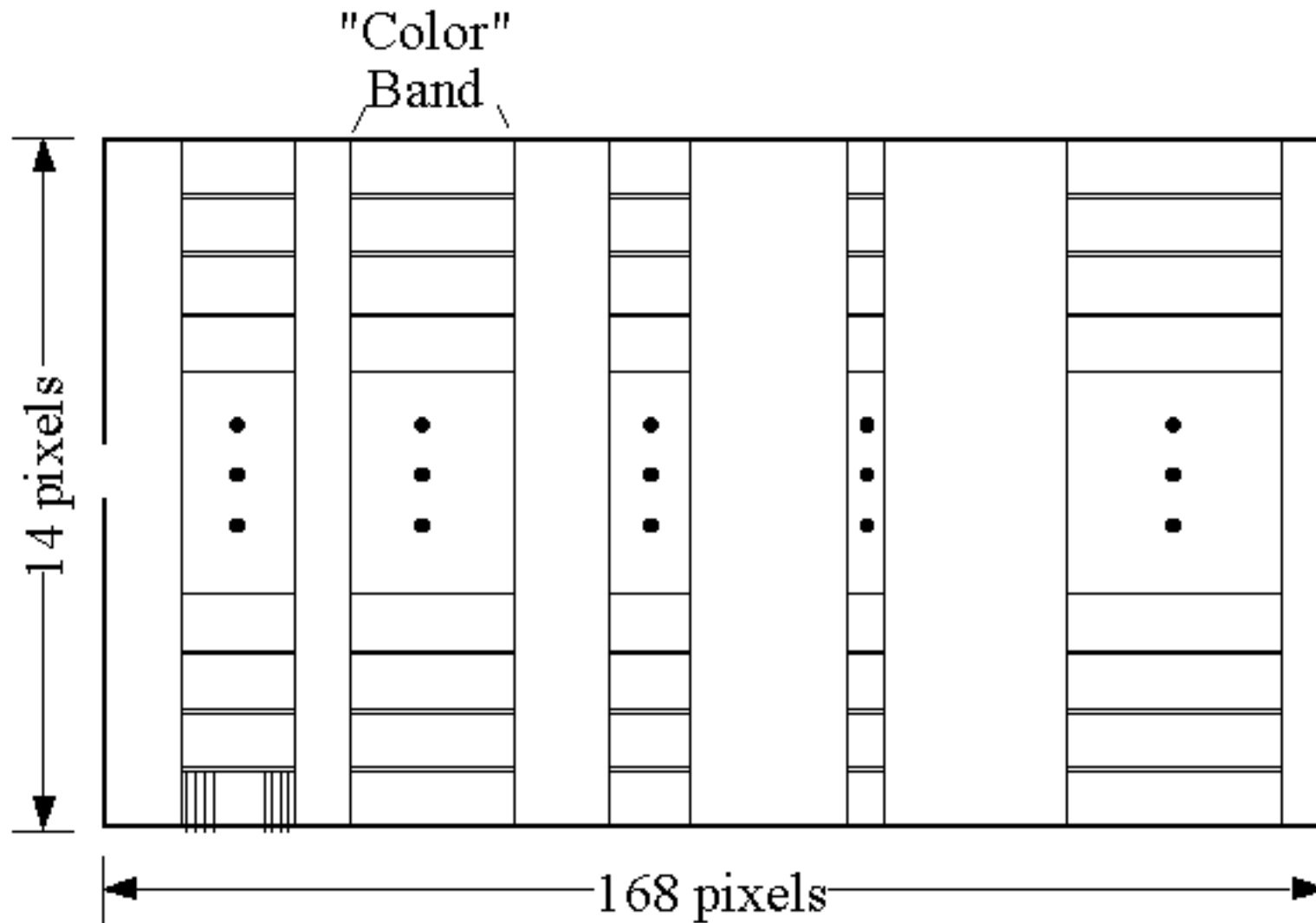
Increment Corresponding Memory Bin



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### Focal Plane Image Accumulation

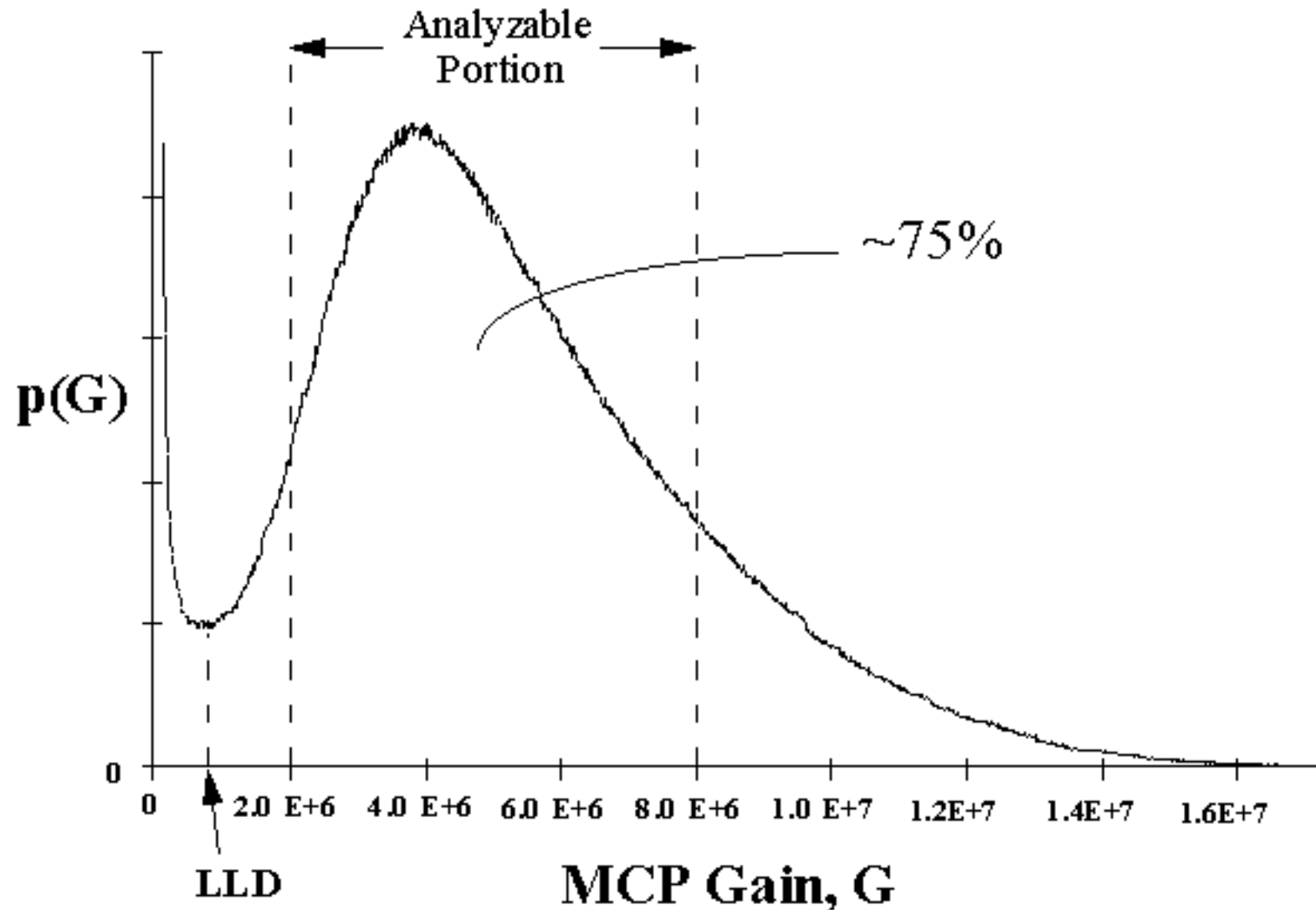




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### MCP Pulse-Height Distribution

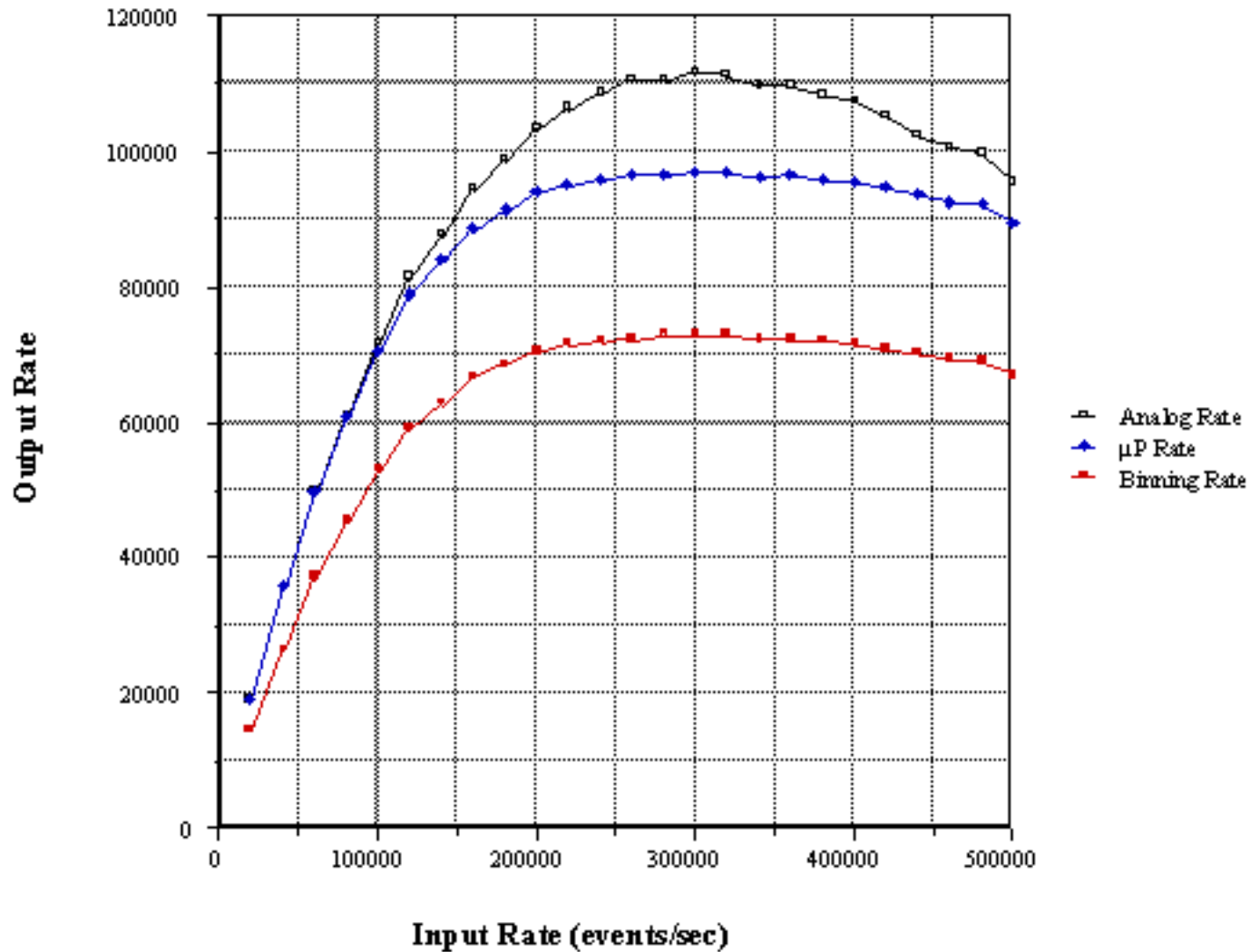




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## Predicted Throughput





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### DPU Commands

- **PROM Based Commands:**
  - Download from CPU to DPU RAM
  - Upload DPU memory to CPU
  - Run PROM test
  - Run RAM test
- **RAM Based Commands:**
  - Initialize DPU
  - Process Detector Data
  - Transfer Data - Imaging Mode
  - Transfer Data - Spectrograph Mode
  - Transfer Data - Individual Pixel Mode
  - Transfer Detector raw W, S, I values
  - Transfer Forced ADC data



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### DPU Commands (Test Port Only)

- **PROM Based Commands:**
  - Download Code - OLS format
  - Dump Memory
  - Alter Memory
  - Dump Registers
  - Print Stack
  - Push data onto Stack
  - Go to program
  - Emit data to test port



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### **Task Level Descriptions**

#### **Download Code**

- Inputs: Starting address location followed by sequential code or data.
- Outputs: None.
- Function: This PROM based routine reads data from the CPU via the communications bus and stores the data into DPU RAM. The first data element is used as a starting address in DPU RAM space. Subsequent code or data elements are stored in sequential memory locations. The routine will wait indefinitely for the next element until the CPU issues a DPU reset.



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### **Task Level Descriptions (cont.)**

#### **Upload Code**

- Inputs: Specified DPU address.
- Outputs: Contents of specified DPU address.
- Function: This PROM based routine reads a single specified address from the CPU and returns the contents of that address (code or data) to the CPU. The DPU will then wait for either a new address or a reset.





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### **Task Level Descriptions (cont.)**

#### **DPU PROM Checksum Test**

- Inputs: None.
- Outputs: Completion code.
- Function: This PROM based routine performs a simple checksum test on the DPU PROM. An error or completion code is returned. The DPU then returns to the main monitor loop.



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### **Task Level Descriptions (cont.)**

#### **DPU RAM Test**

- Inputs: None.
- Outputs: Completion code.
- Function: This PROM based routine checks every memory location in DPU RAM. The test does not preserve previously stored data. The DPU returns an error or completion code before returning to the main monitor loop.



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### Task Level Descriptions (cont.)

#### **Initialize DPU**

- Inputs: FPE Configuration values.
- Outputs: Completion code.
- Function: This task readies the DPU for Detector data processing. The task is comprised of the following subtasks. Write all hardware configuration values to the appropriate hardware control registers. Intialize all variables and arrays. Build the lookup tables for the position computation algorithm.



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### Task Level Descriptions (cont.)

#### Process Detector Data

- Inputs: None.
- Outputs: 2-D Image Accumulation (via transfer commands)
  - Data Structure: 12-bit index = 4-bit spatial position ; 8-bit spectral bin location
- Function: Core dedicated function of the DPU. Poll A/D Board for event. When new event present, read W, S, and I values. Reject event if any of the three values are above upper limit. Compute sum. Reject event if sum below lower limit. Compute x-y position. Bounds check result. Increment pixel value. Return to A/D polling loop. Continue until reset.



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### **Task Level Descriptions (cont.)**

#### **Transfer data - Imaging Mode**

- Inputs: Color Table, 2-D Image accumulation.
- Outputs: For each spatial position, outputs the co-added value for each of the five colors as defined by the color table.
- Function: For each spatial position, sum spectral bins as defined in color table for each color. Color table contains length of color followed by absolute addresses. Return to main monitor loop.



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### **Task Level Descriptions (cont.)**

#### **Transfer data - Spectrograph Mode**

- Inputs: Focal Plane Segment Selection, 2-D Image accumulation.
- Outputs: For each spatial position within chosen focal plane segment, outputs all 160 spectral bin values.
- Function: Begin at segment start position. Transfer 160 spectral bin values. Repeat for each spatial index within segment. Segment definitions TBD. Return to main monitor loop.



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### **Task Level Descriptions (cont.)**

#### **Transfer data - Pixel Mode**

- Inputs: Pixel address, 2-D Image accumulation.
- Outputs: Individual pixel values.
- Function: Read specified absolute address from TLM processor. Transfer pixel value. Wait for new address or reset.



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### **Task Level Descriptions (cont.)**

#### **Transfer data - W, S, I values**

- Inputs: None.
- Outputs: W, S, I values.
- Function: This task collects and transfers raw Wedge, Strip, and Intermediate anode values to the TLM processor. Poll the A/D Board. When new event present, read in three A/D values and transfer data to TLM processor. Return to A/D polling loop. Continue until reset.





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### **Task Level Descriptions (cont.)**

#### **Transfer data - Forced ADC data**

- Inputs: None.
- Outputs: W, S, and I A/D converter values.
- Function: This task collects and transfers raw A/D converter samples to the TLM processor independent of detector activity. Issues a forced conversion command to the A/D Board. Polls for conversion complete. Read in three A/D values and transfer data to TLM processor. Repeat until reset.



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## Detector Processor Software

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